



End Semester Examinations (ESE) of URR-18 B.Tech. III-Semester (SUPPLEMENTARY) APRIL/MAY, 2024

TIME TABLE

Examination Timings: 09:30 AM to 12:30 PM

Date & Day of Examination	CE	ME	EIE	EEE	CSE	IT	ECE	CSN	ECI	CSO (IoT)	CSM (AIML)
25.04.2024 Thursday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
27.04.2024 Saturday	OE-I Strength of Materials	Mechanics of Solids	Object Oriented Programming	Electrical Measurements & Instrumentation	Object Oriented Programming through JAVA	Object Oriented Programming through C++	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
29.04.2024 Monday	Fluid Mechanics	Material Science and Metallurgy	Electrical and Electronic Measurement	Electromagnetic Fields	Discrete Mathematics	Discrete Mathematics	Signals & Systems	---	Signals Systems and Random Processes	Fundamentals of Internet of Things	Formal Languages and Automata Theory
01.05.2024 Wednesday	Surveying	Engineering Thermodynamics	Sensors and Transducers	Network Theory	Computer Architecture and Organization	Computer Architecture and Organization	Network Analysis	Computer Architecture and Organization	Electronic Measurements and Sensors	Computer Organization and Architecture	Computer Organization and Architecture
03.05.2024 Friday	Construction Materials	Machine Drawing	Electronic Devices and Circuits - I	Electronic Devices and Circuits	Advanced Data Structures	Advanced Data Structures	Analog Circuits - I	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
06.05.2024 Monday	---	---	Digital Circuits and Logic Design	---	Digital Electronics	Switching Theory & Logic Design	Switching Theory & Logic Design	Digital Electronics	Digital Circuits and Logic Design	Digital Electronics	--
08.05.2024 Wednesday	--	Essence of Indian Traditional Knowledge	--	--	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	--	Essence of Indian Traditional Knowledge	--	Essence of Indian Traditional Knowledge	--
09.05.2024 Thursday	--	--	--	--	--	Operating Systems	--	Operating Systems Database Management Systems	---	Computer Networks	Operating Systems

Candidate should occupy his/her seat atleast five minutes before the commencement of the examination.

To: All Notice Boards/ Class WhatsApp Groups
 Copy to:- 1. The Principal for information
 2. The Registrar 3. HOD / BoS Chairpersons 4. Dean, Academic Affairs 5. Prof. I/C. Training & Placement 6. Web Team

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Controller of Examinations